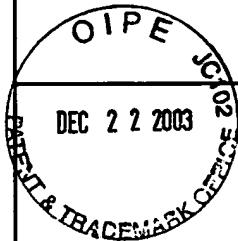


TRANSMITTAL OF FORMAL DRAWINGSDocket No.
POU920030190US1In Re Application Of: **David F. Craddock et al**

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
10/723,506	11/25/2003	NYA	NYA	NYA

Invention: **MEMORY MAPPED INPUT/OUTPUT EMULATION**

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith are:

3 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

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Dated: December 19, 2003

I certify that this document and attached formal drawings are being deposited on 12/19/2003 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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